

GOVERNMENT POLYTECHNIC, TADIPATRI

DIPLOMA IN ELECTRONICS AND COMMUNICATION ENGINEERING SECTION

FACULTY DETAILS



1. Faculty Profile

Name	C RAJENDRA PRASAD
Designation	LECTURER IN ECE
Department	ELECTRONICS AND COMMUNICATION ENGG.
Phone Number	9493773778
Email ID	Raju7ece@gmail.com
Date of joining in the Department	01-11-2013
Date of joining in the Present institute	13-01-2022

2. Educational Qualifications

Qualification	Specialization	Date of Passing(DD-MM-YYYY)	University/Board
UG(B.Tech)	ECE	APRIL-2004	NARAYANA COLLEGE OF ENGINEERING TECHNOLOGY, ALLAGADDA JNTUA, ANNATHAPURAMU
PG	VLSI	31-12-2017	JNTUA, ANATAPUR

3. Teaching & Industry Experience

Experience Type	Institution/ Organization	Duration (From – To)	Total Years
Industry	BSNL,HTD, Hyderabad	08/08/2008 to 31/10/2013	5.5
Teaching	ESC Govt. Polytechnic , NANDYAL	01-11-2013 to 12-01-2022	12
	Govt. Polytechnic, Tadipatri	13-01-2022 to till Date	

5. Research & Publications

S.No	Title Of Paper Publication/Conference	Description Of Publication/Conference	Year
1.			

6. Professional Memberships (Membership in Professional Bodies (IEEE, ISTE, IET, etc.))

S.No	Name Of the Professional Bodies	Type Of Membership	Membership Number

7. Workshops / FDPs / STTPs Attended

S.No	Academic Year	Name Of The Training Program	Institute & Place Of The Training	Duration
1	2025-26	Three weeks industrial Training	NITTTR,Chennai	3-weeks
2	2024-25	Three weeks industrial Training	ECIL,Hyderabad	3-weeks
3	2022-23	Simulation using ORCAD-PSPICE	NITTTR,Chennai	1-Week
4	2021-22	Microchip ATMEGA-16	Eduskills	1Week
5	2019-20	Laboratory Instruction in Analog & Digital Communication	NITTTR,Chennai	1-Week
6	2018-19	MATLAB & LabVIEW Programming	NITTTR,Chennai	1-Week
7	2017-18	INDUCTION TRAINING PROGRAMME	NITTTR,Chennai	2-Week
8	2017-18	Refresher Course in EC Engineering (Practical)	NITTTR,Chennai	1-Week
9	2017-18	Circuit simulation and PCB Design using MULTISIM	NITTTR,Chennai	1-Week
10	2014-15	E-Learning and ICT for Teaching Learning	JNTUH	1-Week

- MOOCs/NPTEL/SWAYAM Certifications:**

S.No	Academic Year	Name Of The Training Program	Institute & Place Of The Training	Duration
1	2025-26	RESEARCH ETHICS AND PLAGIARISM	Igno-NPTEL	8-Week
2	2024-25	VLSI Physical Design with Timing Analysis	NPTEL	12-week
3	2024-25	VLSI Physical Design	NPTEL	12-week
4	2024-25	Intellectual Property Right	NITTTR,Bhopal	8-Week
5	2024-25	Reasearch Methodology	Igno-NPTEL	8-Week
6	2023-25	System Design Through Verilog	NPTEL	8-Week
7	2023-24	Digital Switching-I	NPTEL	8-Week
8	2023-24	System Design Through Verilog	NPTEL	8-Week